

Notice of Allowability

Application No.

09/724,197

Examiner

Jin-Cheng Wang

Applicant(s)

GETTEMY ET AL.

Art Unit

2628

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 10/16/2006.
2. ☒ The allowed claim(s) is/are 1-6,8-14 and 16-23.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |

Reasons for Allowance

Claims 1-6 and 8-9 of the amendment dated 10/16/2006 are allowed. The following is an examiner's statement of reasons for allowance of claims 1-6 and 8-9: Nothing in the prior art anticipates or suggests, "a border attribute register dedicated for containing said border attribute, wherein said border attribute is automatically selected to provide viewing contrast with image data located near said border region, and wherein said border attribute comprises color information for each pixel in said fixed, active pixel border region, and wherein said border attribute is equal to a background attribute currently being displayed" in a display unit comprising: a display panel of a personal digital assistant (PDA) comprising a pixel matrix comprising: a rectangular pixel frame buffer region; and a fixed, active pixel border region permanently dedicated to displaying a border attribute, wherein said fixed, active pixel border region surrounds said rectangular pixel frame buffer region and comprises a width of two pixels; A frame buffer memory for containing image data for generating an image within said frame buffer region; a border attribute register dedicated for containing said border attribute, wherein said border attribute is automatically selected to provide viewing contrast with image data located near said border region, and wherein said border attribute comprises color information for each pixel in said fixed, active pixel border region, and wherein said border attribute is equal to a background attribute currently being displayed; and a display controller coupled to said frame buffer memory, coupled to receive said border attribute from said border attribute register, and coupled to control said display panel, said display controller for generating a first set of signals for rendering said image within said frame buffer region and for generating a second set of signals for displaying said border attribute within said fixed, active border region, set forth in

the independent claim 1.

Claims 10-14 and 16-17 of the amendment dated 10/16/2006 are allowed. The following is an examiner's statement of reasons for allowance of claims 10-14 and 16-17: Nothing in the prior art anticipates or suggests, "a border attribute register dedicated for containing said border attribute for said border region, wherein said border attribute is automatically selected to provide viewing contrast with character images located near said border region, and wherein said border attribute comprises color information for each pixel in said fixed, active pixel border region, and wherein said border attribute is equal to a background attribute" in a display unit comprising: a display panel of a personal digital assistant (PDA) comprising a pixel matrix comprising: a rectangular pixel frame buffer region; and a fixed, active pixel border region permanently dedicated to displaying a border attribute, wherein said fixed, active pixel border region surrounds said rectangular pixel frame buffer region and contains top, bottom, right and left border regions, wherein said fixed, active pixel border region comprises a width of two pixels; a frame buffer memory for containing character data for generating character images within said frame buffer region; a border attribute register dedicated for containing said border attribute for said border region, wherein said border attribute is automatically selected to provide viewing contrast with character images located near said border region, and wherein said border attribute comprises color information for each pixel in said fixed, active pixel border region, and wherein said border attribute is equal to a background attribute; and a display controller coupled to said frame buffer memory, coupled to receive said border attribute of said border attribute register, and coupled to control said display panel, said display controller for generating a first set of

signals for rendering said character images within said rectangular pixel frame buffer region wherein said first set of signals comprises vertical and horizontal invalid timing windows and wherein said display controller is also for generating a second set of signals for displaying said border attribute within said fixed, active pixel border region, set forth in the independent claim 10.

Claims 18-23 of the amendment dated 10/16/2006 are allowed. The following is an examiner's statement of reasons for allowance of claim 18-23: Nothing in the prior art anticipates or suggests, "a border attribute register dedicated for containing said border attribute for said fixed, active border region, wherein said border attribute is automatically selected to provide viewing contrast with image data located near said fixed, active border region, and wherein said border attribute comprises color, and wherein said border attribute is equal to a background attribute currently being displayed" in a portable electronic device comprising: a processor coupled to a bus; a memory unit coupled to said bus; a user input device coupled to said bus; and a display unit coupled to said bus and comprising: a display panel comprising a pixel matrix comprising: a rectangular pixel frame buffer region; and a fixed, active pixel border region permanently dedicated to displaying a border attribute, wherein said fixed, active pixel border region surrounds said rectangular pixel frame buffer region and comprises a width of two pixels; a frame buffer memory for containing image data for generating an image within said rectangular pixel frame buffer region; a border attribute register dedicated for containing said border attribute for said fixed, active border region, wherein said border attribute is automatically selected to provide viewing contrast with image data located near said fixed, active border region, and wherein said border attribute comprises color, and wherein said border attribute is

equal to a background attribute currently being displayed; and a display controller coupled to said frame buffer memory, coupled to receive said border attribute from said border attribute register, and coupled to control said display panel, said display controller for generating a first set of signals for rendering said image within said rectangular pixel frame buffer region and for generating a second set of signals for displaying said border attribute within said fixed, active border region, set forth in the independent claim 18.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."


Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jin-Cheng Wang whose telephone number is (571) 272-7665. The examiner can normally be reached on 8:00 - 6:30 (Mon-Thu).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kee Tung can be reached on (571) 272-7794. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

jcw



KEE M. TUNG
SUPERVISORY PATENT EXAMINER